# AN AUGMENTED INCOMPLETE FACTORIZATION APPROACH FOR COMPUTING THE SCHUR COMPLEMENT IN STOCHASTIC OPTIMIZATION

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Abstract. We present a scalable approach and implementation for solving stochastic optimization problems on high-performance computers. In this work we revisit the sparse linear algebra computations of the parallel solver PIPS with the goal of improving the shared-memory performance and decreasing the time to solution. These computations consist of solving sparse linear systems with multiple sparse right-hand sides and are needed in our Schurcomplement decomposition approach to compute the contribution of each scenario to the Schur matrix. Our novel approach uses an incomplete augmented factorization implemented within the PARDISO linear solver and an outer BiCGStab iteration to efficiently absorb pivot perturbations occurring during factorization. This approach is capable of both efficiently using the cores inside a computational node and exploiting sparsity of the right-hand sides. We report on the performance of the approach on high-performance computers when solving stochastic unit commitment problems of unprecedented size (billions of variables and constraints) that arise in the optimization and control of electrical power grids. Our numerical experiments suggest that supercomputers can be efficiently used to solve power grid stochastic optimization problems with thousands of scenarios under the strict "real-time" requirements of power grid operators. To our knowledge, this has not been possible prior to present work.

**1. Introduction.** In this paper, we present novel linear algebra developments for the solution of linear systems arising in interior-point methods for the solution of structured optimization problems of the form

$$\min_{\substack{x_i, i=0,\dots,N\\ s.t.}} \left( \frac{1}{2} x_0^T Q_0 x_0 + c_0^T x_0 \right) + \frac{1}{N} \sum_{i=1}^N \left( \frac{1}{2} x_i^T Q_i x_i + c_i^T x_i \right) \\
\text{s.t.} \quad T_0 x_0 &= b_0, \\
T_1 x_0 + W_1 x_1 &= b_1, \\
T_2 x_0 + W_2 x_2 &= b_2, \\
\vdots & \ddots & \vdots \\
T_N x_0 + W_N x_N &= b_N, \\
x_0 \ge 0, \quad x_1 \ge 0, \quad x_2 \ge 0, \quad \dots \quad x_N \ge 0. \\$$
(1.1)

Optimization problems of the form (1.1) are known as convex quadratic optimization problems with *dual block-angular* structure. Such problems arise as the *extensive form* in stochastic optimization, being either deterministic equivalents or sample average approximations (SAAs) of two-stage stochastic optimization problems with recourse [23]. Two-stage stochastic optimization problems are formulated as

$$\min_{x} \left( \frac{1}{2} x_0^T Q_0 x + c^T x_0 \right) + \mathbb{E}_{\xi} [G(x_0, \xi)] 
\text{s.t. } T_0 x_0 = b_0, x_0 \ge 0,$$
(1.2)

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where the *recourse function*  $G(x_0, \xi)$  is defined by

$$G(x_0, \xi) = \min_{x} \frac{1}{2} x^T Q_{\xi} x + c_{\xi}^T x$$
  
s.t.  $T_{\xi} x_0 + W_{\xi} x = b_{\xi}, x \ge 0.$  (1.3)

The expected value  $\mathbb{E}[\cdot]$ , which is assumed to be well defined, is taken with respect to the random variable  $\xi$ , which contains the data  $(Q_{\xi}, c_{\xi}, T_{\xi}, W_{\xi}, b_{\xi})$ . The SAA problem (1.1) is obtained by generating N samples  $(Q_i, c_i, T_i, W_i, b_i)$  of  $\xi$  and replacing the expectation operator with the sample average. The matrix  $Q_{\xi}$  is assumed to be positive semidefinite for all possible  $\xi$ .  $W_{\xi}$ , the *recourse* matrix, is assumed to have full row rank.  $T_{\xi}$ , the *technology* matrix, need not have full rank. The deterministic matrices  $Q_0$  and  $T_0$  are assumed to be positive semidefinite and of full row rank, respectively. The variable  $x_0$  is called the *first-stage* decision, which is a decision to be made now. The *second-stage* decision x is a *recourse* or corrective decision that one makes in the future after some random event occurs. The stochastic optimization problem finds the optimal decision to be made now that has the minimal expected cost in the future.

Stochastic optimization is one of the main sources of extremely large dual block-angular problems. SAA problems having billions of variables can be easily obtained in cases when a large number of samples is needed to accurately capture the uncertainties. Such instances necessitate the use distributed-memory parallel computers. Dual block-angular optimization problems are also natural candidates for decomposition techniques that take advantage of the special structure. Existing parallel decomposition procedures for the solution of dual angular problems are reviewed by Vladimirou and Zenios [26]. Subsequent to their review, Linderoth and Wright [11] developed an asynchronous approach combining  $l_{\infty}$  trust regions with Benders decomposition on a large computational grid. Decomposition inside standard optimization techniques applied to the extensive form has been implemented in the state-of-the-art software package OOPS [8] as well as by some of the authors in PIPS-IPM [13, 14, 17] and PIPS-S [12]. OOPS and PIPS-IPM implement interior-point algorithms and PIPS-S implements the revised simplex method. The decomposition is obtained by specializing linear algebra to take advantage of the dual block-angular structure of the problems.

This paper revisits linear algebra techniques specific to interior-point methods applied to optimization problems with dual block-angular structure. Decomposition of linear algebra inside interior-point methods is obtained by applying a Schur complement technique (presented in Section 3). This approach requires at each iteration of the interior-point method solving a dense linear system (the Schur complement) and a substantial number of sparse linear systems for each scenario (needed to compute each scenario's contribution to the Schur complement). For a given scenario, the sparse systems of equations share the same matrix, a situation described in the linear algebra community as solving linear systems with *multiple right-hand sides*. The number of the right-hand sides is large relative to the number of the unknowns or equations for many stochastic optimization problems, which is unusual in traditional linear algebra practices. Additionally, the right-hand sides are extremely sparse, a feature that should be exploited to reduce the number of floating-point operations.

In PIPS-IPM we have previously used off-the-shelf linear solvers (MA57 and WSMP) and solved for each right-hand side (in fact for small blocks of right-hand sides). This approach has two important drawbacks: (1) the sparsity of the right-hand sides is not exploited, sparse right-hand sides being a rare feature of linear solvers; and (2) the calculations limits the efficient use of multicore shared-memory environments because the triangular solves do not scale well with the number of cores.

In this work we propose a novel reformulation of the sparse linear algebra calculations

that circumvents these issues and considerably speeds the calculations. We employ an augmented factorization technique for solving the sparse linear systems with multiple right-hand sides and a preconditioned Krylov subspace method to cheaply absorb the pivot perturbations that occur from the factorization. These new algorithmic developments and their implementation details in PARDISO and PIPS-IPM are presented in Section 3. In Section 4 we study the large-scale computational performance and parallel efficiency on different high-performance computing platforms when solving stochastic optimization problems of up to 2 billion variables and up to almost 2 billion constraints. Such problems arise in the optimization of the electrical power grid and are described in Section 2.

Notation and terminology Lower-case Latin characters are used for vectors, and uppercase Latin characters are used for matrices. Lower-case Greek characters denote scalars. Borrowing the terminology from stochastic optimization, we refer to the zero-indexed variables or matrices of problem (1.1) as "first stage," the rest as "second stage." By a scenario i (i = 1, ..., N) we mean the first-stage data ( $Q_0$ ,  $c_0$  and  $T_0$ ) and second-stage data  $Q_i$ ,  $c_i$ ,  $T_i$  and  $W_i$ . Unless stated otherwise, lower indexing of a vector indicates the first-stage or second-stage part of the vector (not its components). Matrices Q and A are a compact representation of the quadratic term of the objective and of the constraints:

$$Q = \left[ egin{array}{cccc} Q_0 & & & & & \ & Q_1 & & & & \ & & \ddots & & \ & & & Q_N \end{array} 
ight], ext{ and } A = \left[ egin{array}{cccc} T_0 & & & & \ T_1 & W_1 & & & \ dots & & \ddots & & \ T_N & & & W_N \end{array} 
ight].$$

#### 2. Motivating Application: Stochastic Unit Commitment for Power Grid Systems.

Today's power grid will require significant changes in order to handle increased levels of renewable sources of energy; these sources, such as wind and solar, are fundamentally different from traditional generation methods because they cannot be switched on at will. Instead, their output depends on the weather and may be highly variable within short time periods. Figure 2.1 illustrates the magnitude and frequency of wind supply fluctuations under hypothetical adoption levels compared with a typical total load profile in Illinois. Uncertainty in weather forecasts and other risks such as generator and transmission line failure are currently mitigated by using conservative reserve levels, which typically require extra physical generators operating so that their generation levels may be increased on short notice. Such reserves can be both economically and environmentally costly. Additionally, inevitable deviations from output levels estimated by weather forecasts can lead to inefficiencies in electricity markets manifested as wide spatiotemporal variations of prices (see Figure 2.2).

Stochastic optimization has been identified in a number of studies as a promising mathematical approach for treating such uncertainties in order to reduce reserve requirements and stabilize electricity markets in the next-generation power grid [2, 3, 18]. Constantinescu et al. [3] made the important empirical observation that while reality may deviate significantly from any single weather forecast, a suitably generated family of forecasts using numerical weather prediction and statistical models can capture spatiotemporal variations of weather over wide geographical regions. Such a family of forecasts naturally fits within the paradigm of stochastic optimization when considered as samples (scenarios) from a distribution on weather outcomes.

Computational challenges, however, remain a significant bottleneck and barrier to real-world implementation of stochastic optimization of energy systems. This paper is the latest in a line of work [14, 13, 17] intended to address these challenges by judicious use of linear algebra and parallel computing within interior-point methods.

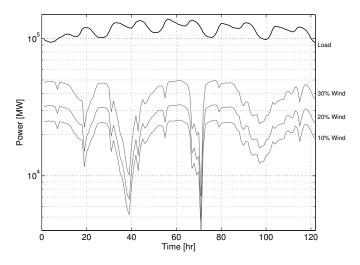


FIG. 2.1. Snapshot of total load and wind supply variability at different adoption levels.

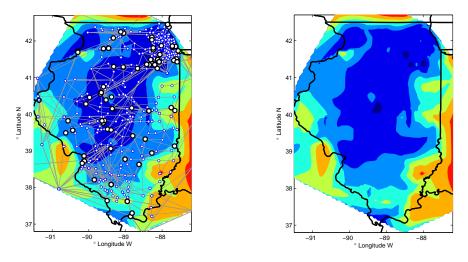


FIG. 2.2. Snapshot of the price distribution across the state of Illinois under deterministic (left) and stochastic (right) formulations. We note more uniform prices in the interior of the state under stochastic optimization, as this approach enables the anticipation of uncertainties. Transmission grid illustrated on left. Figures credit Victor Zavala.

Power-grid operators (ISOs) solve two important classes of optimization problems as part of everyday operations; these are unit commitment (UC) and economic dispatch (ED) [22]. UC decides the optimal on/off schedule of *thermal* (coal, nuclear) generators over a horizon of 24 hours or longer (California currently uses 72-hour horizons). This is a nonconvex problem that is typically formulated as a mixed-integer linear optimization problem and solved by using commercial software [24]. In practice, a near-optimal solution to the UC problem must be computed within an hour.

In our analysis, we consider a two-stage stochastic optimization formulation for unit

commitment. The problem has the following structure (c.f. [14]):

$$\min\left(\sum_{k=0}^{T} \sum_{j \in \mathscr{G}} f_j \cdot x_{k,j}\right) + \frac{1}{N} \sum_{s \in \mathscr{N}} \left(\sum_{k=0}^{T} \sum_{j \in \mathscr{G}} c_j \cdot G_{s,k,j}\right)$$
(2.1a)

s.t. 
$$G_{s,k+1,j} = G_{s,k,j} + \Delta G_{s,k,j}, s \in \mathcal{N}, k \in \mathcal{T}, j \in \mathcal{G}$$
 (2.1b)

$$\sum_{(i,j) \in \mathcal{L}_j} P_{s,k,i,j} + \sum_{i \in \mathcal{G}_j} G_{s,k,i} = \sum_{i \in \mathcal{D}_j} D_{s,k,i}$$

$$-\sum_{i\in\mathscr{W}_{i}}W_{s,k,i}, s\in\mathscr{N}, k\in\mathscr{T}, j\in\mathscr{B} \qquad (p_{s,k,j})$$
(2.1c)

$$P_{s,k,i,j} = b_{i,j}(\theta_{s,k,i} - \theta_{s,k,j}), s \in \mathcal{N}, k \in \mathcal{T}, (i,j) \in \mathcal{L}$$
(2.1d)

$$0 \le G_{s,k,j} \le x_{k,j} G_j^{max}, s \in \mathcal{N}, k \in \mathcal{T}, j \in \mathcal{G}$$
(2.1e)

$$|\Delta G_{s,k,j}| \le \Delta G_j^{max}, s \in \mathcal{N}, k \in \mathcal{T}, j \in \mathcal{G}$$
(2.1f)

$$|P_{s,k,i,j}| \le P_{i,j}^{max}, s \in \mathcal{N}, k \in \mathcal{T}, (i,j) \in \mathcal{L}$$
 (2.1g)

$$|\theta_{s,k,j}| \le \theta_j^{max}, s \in \mathcal{N}, k \in \mathcal{T}, j \in \mathcal{B}$$
 (2.1h)

$$x_{k,j} \in \{0,1\}, k \in \mathcal{T}, j \in \mathcal{G} \tag{2.1i}$$

Here,  $\mathcal{G}$ ,  $\mathcal{L}$ , and  $\mathcal{B}$  are the sets of generators, lines, and transmission nodes (intersections of lines, known as buses) in the network in the geographical region, respectively.  $\mathcal{D}_j$  and  $\mathcal{W}_j$  are the sets of demand and wind-supply nodes connected to bus j, respectively. The symbol  $\mathcal{N}$  denotes the set of scenarios for wind level and demand over the time horizon  $\mathcal{T} := \{0, ..., T\}$ . The *first stage* decision variables are the generator on/off states  $x_{k,j}$  over the complete time horizon. The decision variables in each second-stage scenario s are the generator supply levels  $G_{s,k,j}$  for time instant s, and bus s, the transmission line power flows s, and the bus angles s, which are related to the *phase* of AC current). The random data in each scenario are the wind supply flows s, and the demand levels s, across the network. The values of s, and s, are fixed by initial conditions.

Constraint (2.1c) balances flow across the network according to Kirchoff's law. This constraint introduces the structure of the transmission grid into the problem. Constraints (2.1f) are the so-called ramp constraints that restrict how quickly generation levels can change. The objective function contains the fixed costs  $f_j$  for operating a generator and the generation costs  $c_j$ . Generation costs are convex and are more accurately modeled as quadratic, although in practice they are treated as linear and piecewise linear functions for simplicity. Our solver is equally capable of handling both cases, but in our test problems we used the linear form.

Note that the scenarios are coupled only by the constraint (2.1e), which enforces that the generation level in each scenario be zero if the generator is off at a particular time. Under this model, the on/off states are chosen such that (1) under each scenario, there is a feasible generation schedule, and (2) the (approximate) expected value of the generation costs is minimized.

In this work, we consider solving a convex relaxation of (2.1) obtained by replacing the binary restrictions (2.1i) with the constraints  $0 \le x_{k,j} \le 1$ . This relaxation is a linear optimization problem that would be solved at the "root node" of branch-and-bound approaches for solving (2.1). Empirically, deterministic UC problems have been observed to have a small gap between the optimal value of the convex relaxation and the true optimal solution when combined with cutting-plane techniques and feasibility heuristics, which are standard practice in mixed-integer optimization. This often implies that a sufficiently small gap is obtainable with little or no enumeration of the branch-and-bound tree [24]. We believe it is reasonable to first focus our efforts on the computational tractability of the convex relaxation. Our

developments apply directly to stochastic ED formations as well.

The convex relaxation of (2.1) itself is an extremely large-scale linear optimization problem. Our model incorporates the transmission network of the state of Illinois, which contains approximately 2,000 transmission nodes, 2,500 transmission lines, 900 demand nodes, and 300 generation nodes (illustrated in Figure 2.2). A deterministic formulation over this geographical region can have as many as 100,000 variables and constraints. In our stochastic formulation, this number of variables and constraints is effectively multiplied by the number of scenarios. As scenarios effectively correspond to samples in a high-dimensional Monte-Carlo integration, it is reasonable to desire the capability to use thousands if not tens of thousands; hence total problem sizes of tens to hundreds of millions of variables, which are presently far beyond the capabilities of commercial solvers, are easily obtainable. The number of variables in the first-stage block  $x_0$  is the number of generators times the number of time steps, leading to sizes of 10,000 or more, which makes parallel decomposition non-trivial.

- **3.** Computational Approach and Implementation. In this section we first provide a compact presentation of the Mehrotra's primal-dual path-following algorithm and the Schur complement-based decomposition of the linear algebra from PIPS. After that we present the novel approach for computing the Schur complement.
- **3.1. Interior-point method.** Let us consider a general form of a quadratic programming (QP) problem:

$$\min \frac{1}{2}x^T Q x + c^T x \text{ subject to } A x = b, \ x \ge 0.$$
 (3.1)

We consider only convex QPs (Q needs to be positive semi-definite) and linear programming (LP) problems (Q = 0). Additionally, the matrix A is assumed to have full row rank. Observe that stochastic programming problem (1.1) is a convex QP.

Path-following interior-point methods for the solution of problem (3.1) make use of the "central path," which is a continuous curve  $(x(\mu), y(\mu), z(\mu)), \mu > 0$ , satisfying

$$Qx - A^{T}y - z = -c,$$

$$Ax = b,$$

$$xz = \mu e,$$

$$x,z > 0.$$
(3.2)

Here  $y \in \mathbb{R}^m$  and  $z \in \mathbb{R}^n$  correspond to the Lagrange multipliers,  $e = [1 \ 1 \ \dots \ 1]^T \in \mathbb{R}^n$ , and  $z \in \mathbb{R}^n$  denotes the componentwise product.

In the case of a feasible problem (3.1), the above system has a unique solution  $(x(\mu), y(\mu), z(\mu))$  for any  $\mu > 0$ , and, as  $\mu$  approaches zero,  $(x(\mu), y(\mu), z(\mu))$  approaches a solution of (3.1), see Chapter 2 in [28]. A path-following method is an iterative numerical process that follows the central path in the direction of decreasing  $\mu$  toward the solution set of the problem. The iterates generated by the method generally do not stay on the central path. Rather, they are located in a controlled neighborhood of the central path that is a subset of the positive orthant.

In the past two decades, predictor-corrector methods have emerged as practical path-following IPMs in solving linear and quadratic programming problems. Among the predictor-corrector methods, the most successful is Mehrotra's predictor-corrector algorithm. Although Mehrotra [16] presented his algorithm in the context of linear programming, it has been successfully applied also to convex quadratic programming [6] and standard monotone linear complementarity problems [29]. It also has been widely used in the implementation of several IPM-based optimization packages, including OB1 [15], HOPDM [7], PCx [4], LIPSOL [30], and OOQP [6].

Two linear systems of the form (3.3) are solved at each IPM iteration one to compute predictor search direction and one to compute corrector search directions. For a detailed description of Mehrotra's method used in this paper we refer the reader to [6] and [16, 28]. Let us denote the kth IPM iteration by  $(x_k, y_k, z_k)$ . Also let  $X_k$  and  $Z_k$  denote the diagonal matrices with the (positive) entries given by  $x_k$  and  $z_k$ . The linear system solved during both the predictor and corrector phase to obtain the search direction  $(\Delta x_k, \Delta y_k, \Delta z_k)$  is

$$Q\Delta x_k - A^T \Delta y_k - \Delta z_k = r_k^1$$

$$A\Delta x_k = r_k^2$$

$$Z_k \Delta x_k + X_k \Delta z_k = r_k^3.$$
(3.3)

While the right-hand sides  $r_k^1$ ,  $r_k^2$ , and  $r_k^3$  are different for the predictor and the corrector, the matrix remains the same (this feature of the Mehrotra's algorithm gives important computational savings, since only one factorization, not two, per IPM iteration is required).

By performing block elimination for  $\Delta z_k$ , the linear systems (3.3) can be reduced to the following symmetric indefinite linear system,

$$\begin{bmatrix} Q + D_k^2 & A^T \\ A & 0 \end{bmatrix} \begin{bmatrix} \Delta x_k \\ -\Delta y_k \end{bmatrix} = \begin{bmatrix} r_k^1 + X_k^{-1} r_k^3 \\ r_k^2 \end{bmatrix}, \tag{3.4}$$

where  $D_k = X_k^{-\frac{1}{2}} Z_k^{\frac{1}{2}}$ .

Factorizing the matrix of system (3.4) and then solving twice with the factors accounts for most of the computational effort of each interior-point iteration. Section 3.2 present the algorithmic and implementation details of performing these operations in parallel for stochastic optimization problems.

The remaining fraction of the computations is required by the computations the residuals of (3.4) (mat-vec operations Qx, Ax, and  $A^Ty$ ), linesearch and iteration updates (vec-vec operations  $x = x + \alpha \Delta x$ ), as well as stopping criteria (vector norms). In PIPS we exploit the special structure of the problem data to distribute both the data and all computations across computational nodes. Data distribution is done by partitioning the scenarios and assigning a partition to each computational node (partitions have an equal or close to equal number of scenarios in order to avoid load imbalance). The first-stage data  $(Q_0, T_0)$  and variables  $(x_0, y_0)$  are replicated across nodes in order to avoid extra communication. Mat-vecs Qx, Ax and  $A^Ty$  can be efficiently parallelized for stochastic optimization. For example r = Qx can be done without communication since Q is block diagonal, each node computing  $r_0 = Q_0 r_0$  and  $r_i = Q_i x_i$  ( $i \ge 1$ ) for scenarios i that were assigned to it. The mat-vec r = Ax also requires no communication; each node computes  $r_0 = T_0 x_0$  and  $r_i = T_i x_0 + W_i x_i$ . For the mat-vec

$$r = A^{T}y = \begin{bmatrix} T_{0}^{T}y_{0} + \sum_{i=1}^{N} T_{i}^{T}y_{i} & & & \\ W_{1}^{T}y_{1} & & & & \\ & & W_{N}^{T}y_{N} & & & \end{bmatrix},$$

each node n computes  $r_i = W_i^T y_i$  and  $\tilde{r}_0^n = \sum_i T_i^T y_i$  for each of its scenarios i and performs an "all-reduce" communication to sum  $\tilde{r}_0^n$  across nodes and calculate  $r_0 = T_0^T y_0 + \sum_{i=1}^N T_i^T y_i$ . The vec-vec operations are parallelized in a similar way. The same approach is used in OOPS; we refer the reader to [8] for a detailed description.

**3.2. Linear algebra overview.** In PIPS we exploit the arrow-shaped structure of the optimization problem (1.1) to produce highly parallelizable linear algebra. The linear system

that is solved at each IPM iteration can be reordered in the primal-dual angular form

$$K := \begin{bmatrix} K_1 & & B_1 \\ & \ddots & & \vdots \\ & & K_N & B_N \\ B_1^T & \dots & B_N^T & K_0 \end{bmatrix}, \tag{3.5}$$

where 
$$K_0 = \begin{bmatrix} Q_0 + D_0 & T_0^T \\ T_0 & 0 \end{bmatrix}$$
,  $K_i = \begin{bmatrix} Q_i + D_i & W_i^T \\ W_i & 0 \end{bmatrix}$  and  $B_i = \begin{bmatrix} 0 & 0 \\ T_i & 0 \end{bmatrix}$ ,  $i = 1, ..., N$ .  $D_0, D_1, ..., D_N$  are diagonal matrices with positive entries arising from the use of interior-point methods and change at each IPM iteration.

It is well known in the linear algebra community that primal-dual angular linear systems of the form (3.5) can be parallelized by using a Schur complement technique. We follow the same approach in PIPS and present it here for completeness. By performing a block Gaussian elimination of the bordering blocks, one can solve the linear system (3.5) by

I. computing the Schur complement

$$C = K_0 - \sum_{i=1}^{N} B_i^T K_i^{-1} B_i; (3.6)$$

II. solving Schur linear system

$$C\Delta z_0 = r_0 - \sum_{i=1}^{N} B_i^T K_i^{-1} r_i;$$
 (3.7)

III. solving second-stage linear systems (i = 1, ..., N)

$$K_i \Delta z_i = B_i \Delta z_0 - r_i. \tag{3.8}$$

Most of the computations – in particular, obtaining the scenarios contributions  $B_i^T K_i^{-1} B_i$  to the Schur complement, computing the residual in step II and solving for  $\Delta z_i$  – can be performed independently, yielding an efficient parallelization scheme. However, solving with the dense Schur complement C in step II may be a parallelization bottleneck for problems having a large number of first-stage variables. The bottleneck can be overcome by distributing C and solving the Schur complement linear systems in parallel [13]. By default, PIPS uses LAPACK and multithreaded BLAS to factorize and solve with C on each node.

Algorithm 1 lists the parallel procedure we use in PIPS to solve  $K\Delta z = r$  using the Schur complement-based decomposition scheme (3.6)-(3.8). The verb "reduce" refers to the communication operation that combines data held by different processes through an associative operator, in our case summation, and accumulates the result on a single process (reduce) or on all processes (all-reduce). MPI routines MPI\_Reduce and MPI\_Allreduce correspond to these operations. A different communication strategy is also available in PIPS. Instead of all-reducing C in step 3 and replicating the factorization in step 4 on all nodes, one could only reduce C to process 1 and perform the factorization on process 1 only. Consequently, in step 6  $v_0$  is reduced only to process 1, which then performs step 7 and broadcasts (MPI\_Bcast)  $v_0$  to the rest of the processes. In theory this communication pattern should be faster than the one of Algorithm 1, especially since the most expensive communication, all-reducing C, is avoided. However, it is slower on "Intrepid" BG/P because all-reduce is about two times faster than reduce (this anomaly is likely to be caused by an implementation problem of BG/P's MPI\_Reduce or lack of optimization).

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Given the set \mathscr{P}=\{1,2,\ldots,P\} processes, distribute N scenarios evenly across \mathscr{P} and let \mathscr{N}_p be the set of scenarios assigned to process p\in\mathscr{P}. Each process p\in\mathscr{P} executes the following procedures: (factorization\ phase)

Function (L_1,D_1,\ldots,L_N,D_N,L_C,D_C)=Factorize(K)

Factorize L_iD_iL_i^T=K_i for each i\in\mathscr{N}_p.

Compute SC contribution S_i=B_i^TK_i^{-1}B_i for each i\in\mathscr{N}_p and accumulate C_p=-\sum_{i\in\mathscr{N}_p}S_i. On process 1, let C_1=C_1+K_0.

All-reduce SC matrix C=\sum_{r\in\mathscr{P}}C_r.

Factorize SC matrix L_cD_cL_c^T=C; (solve\ phase)

Function \Delta z=Solve(L_1,D_1,\ldots,L_N,D_N,L_C,D_C,r)

Solve w_i=K_i^{-1}r_i=L_i^{-T}D_i^{-1}L_i^{-1}r_i for each i\in\mathscr{N}_p and compute v_p=\sum_{i\in\mathscr{N}_p}B_i^Tw_i.

On process 1, let v_1=v_1+r_0.

All-reduce v_0=\sum_{i\in\mathscr{N}_p}v_i.

Solve \Delta z_0=C^{-1}v_0=L_c^{-T}D_c^{-1}L_c^{-1}v_0.

Solve \Delta z_0=K_i^{-1}(B_i\Delta z_0-r_i)=L_i^{-T}D_i^{-1}L_i^{-1}(B_i\Delta z_0-r_i) for each i\in\mathscr{N}_p.
```

The computation of the Schur complement (step 2 of Algorithm 1) was by far the most expensive operation and it was traditionally done in PIPS [14] by solving with the factors of  $K_i$  for each nonzero column of  $B_i$  and multiplying the result from the left with  $B_i^T$ . A slightly different approach is to apply a primal-dual regularization [1] to the optimization problem to obtain quasidefinite matrices  $K_i$  that are strongly factorizable to a form of Cholesky-like factorization  $K_i = L_i L_i^T$  and to compute  $B_i^T K_i^{-1} B_i$  as sparse outer products of  $L_i^{-1} B_i^T$ . This approach is implemented in OOPS [8]. In both cases, the computational burden is on solving with *sparse factors* of  $K_i$  against *multiple sparse right-hand sides*.

**3.3. Computing**  $B_i^T K_i^{-1} B_i$  **using the augmented approach.** In many computational science applications, the numerical factorization phase  $K_i = L_i D_i L_i$  of forming the partial Schur-complement contributions  $S_i = B_i^T K_i^{-1} B_i$  has generally received the most attention, because it is typically the largest component of execution time; most of the algorithmic improvements [5, 19, 21] in the factorization are related to the exploitation of the sparsity structure in  $K_i$ . In PIPS, however, the solve step  $K_i^{-1} B_i$  dominates and is responsible for a much higher proportion of the memory traffic. This makes it a bottleneck in PIPS on multicore architectures that have a higher ratio of computational power to memory bandwidth.

The multicore architectures that emerged in recent years brought substantial increases in the number of processor cores and their clock rates but only limited increases in the speed and bandwidth of the main memory. For this reason, in many application codes processor cores spend considerable time in accessing memory, causing a performance bottleneck known the "memory bandwidth wall." This adverse behavior is likely to be aggravated by the advent of many-core architectures (having hundreds of cores per chip), because it is expected that the

speed and bandwidth of the memory will virtually remain unchanged.

In our computational approach, the memory bandwidth wall occurs when solving with the factors of  $K_i$ . This is because triangular solves are known to parallelize poorly on multicore machines [10]. In PIPS the number of the right-hand sides can be considerably large (for some problems  $B_i$  has more than 10,000 columns), and most of the execution time is spent in solving with the factors of  $K_i$ , causing an inefficient utilization of the core for most of the time. Even more important, the right-hand sides  $B_i$  are very sparse, a feature that can considerably reduce the number of arithmetic operations. However, exploiting the sparsity of the right-hand sides during the triangular solve increases the memory traffic and exacerbates the memory bandwidth wall.

On the other hand, indefinite sparse factorizations can achieve good speed-ups on multi-cores machines [20, 10]. We propose an augmented factorization-based technique for computing  $B_i^T K_i^{-1} B_i$ . Our approach consists of performing a partial factorization of the augmented matrix

$$M_i = \left[ \begin{array}{cc} K_i & B_i^T \\ B_i & 0 \end{array} \right]. \tag{3.9}$$

More specifically, the factorization of the augmented matrix is stopped after the first  $k_i$  pivots  $(k_i)$  being the dimension of  $K_i$ ). At this point in the factorization process, the lower right block of the factorization contains the exact Schur complement matrix  $-B_i^T K_i^{-1} B_i$ .

**3.4. Solution refinement.** The factorization of  $M_i$  is based on a static Bunch-Kaufman pivoting developed in [20] and implemented in the solver PARDISO [21]. A similar approach is available as an option in WSMP [9]. In this case, the coefficient matrix is perturbed whenever numerically acceptable  $1 \times 1$  and  $2 \times 2$  pivots cannot be found within a diagonal supernode block (checks on potential pivots are made only within the supernode block). This method has been shown to perform well in many applications. A downside that is not negligible is that solving with  $K_i$  might require an increased number of refinement steps to achieve the requested accuracy in the interior-point optimization algorithm [21].

In our case the pivot perturbations during the factorization of  $K_i$  also propagate in  $S_i = B_i^T K_i^{-1} B_i$ . Absorbing these perturbations through iterative refinement [27] requires solving with  $K_i$  for each column of  $B_i$ , an operation that we wanted to avoid in the first place. Instead, we let the perturbations propagate through the factorization phase in the Schur complement C. At the end of the factorization phase of Algorithm 1 we have an implicit factorization of a perturbed matrix

$$\tilde{K} := \begin{bmatrix} \tilde{K}_1 & B_1 \\ & \ddots & \vdots \\ & \tilde{K}_N & B_N \\ B_1^T & \dots & B_N^T & K_0 \end{bmatrix}, \tag{3.10}$$

where  $\tilde{K}_i$ , i = 1, ..., N, denote the perturbed matrices that were factorized during the (incomplete) factorization of  $M_i$ .

It is essential that the solve phase account for and absorb the perturbations; otherwise the interior-point directions are inaccurate, and the optimization stalls. Our first approach was to perform iterative refinement for  $K\Delta z = r$ ; however, this technique showed a large variability in the number of steps. In comparison, the bi-conjugate gradient stabilized (BiCGStab) method [25] proved to be more robust, seemingly able to handle large perturbations more efficiently than iterative refinement. BiCGStab is a Krylov subspace that solves unsymmetric systems of equations and was designed as a numerically stable variant of the bi-conjugate

## Algorithm 2

Solving a linear system Kz = b using preconditioned BiCGStab, with the preconditioner  $\tilde{K}$  being applied from the right. Implicit factorization of  $\tilde{K}$  is provided as input. Here  $z_i$  denotes the ith iterate of the algorithm.

```
Function z = BiCGStabSolve(K, \tilde{L}_1, \tilde{D}_1, \dots, \tilde{L}_N, \tilde{D}_N, \tilde{L}_C, \tilde{D}_C, b)
    Compute inital guess z_0 = \text{Solve}(\tilde{L}_1, \tilde{D}_1, \dots, \tilde{L}_N, \tilde{D}_N, \tilde{L}_C, \tilde{D}_C, b)
     Compute initial residual r_0 = b - Kz_0.
    Let \tilde{r}_0 be an arbitrary vector such that \tilde{r}_0^T r_0 \neq 0 (for example \tilde{r}_0 = r_0).
    \rho_0 = \alpha = \omega_0 = 1; v_0 = p_0 = 0.
    For i = 1, 2, ...

\rho_i = \tilde{r}_0^T r_{i-1}; \beta = (\rho_i/\rho_{i-1})(\alpha/\omega_{i-1}).

         p_i = r_{i-1} + \beta(p_{i-1} - \omega_{i-1}v_{i-1}).
         (preconditioner application y = \tilde{K}^{-1}p_i)
         y = \text{Solve}(\tilde{L}_1, \tilde{D}_1, \dots, \tilde{L}_N, \tilde{D}_N, \tilde{L}_C, \tilde{D}_C, p_i).
         (matrix application)
         v = Ky;
         \alpha = \rho_i/(\tilde{r}_0, v_i);
         s = r_{i-1} - \alpha v_i;
         z_i = z_{i-1} + \alpha y;
         If ||s|| is small enough then return z_i if ||Kz_i - b|| is small enough;
         (preconditioner application w = \tilde{K}^{-1}s)
         w = \text{Solve}(\tilde{L}_1, \tilde{D}_1, \dots, \tilde{L}_N, \tilde{D}_N, \tilde{L}_C, \tilde{D}_C, s)
         (matrix application)
         t = Kw;
         \omega_i = (t^T s)/(t^T t);
         z_i = z_i + \omega_i w;
         If ||w|| is small enough then return z_i if ||Kz_i - b|| is small enough;
         r_i = s - \omega_i t;
    end
```

gradient method. For symmetric indefinite linear systems such as ours, BiCGStab also outperforms classical conjugate gradient method in terms of numerical stability.

As any other Krylov subspace method, the performance of BiCGStab strongly depends on the use of a preconditioner and on the quality of the preconditioner. Since  $\tilde{K}$  is a close approximation of K, the obvious choice for the preconditioner in our case is  $\tilde{K}^{-1}$ .

Alternatively one could absorb the errors by performing iterative refinement independently when solving the Schur linear system  $C\Delta z_0 = v_0$  (step 7 in Algorithm 1) and the second-stage linear systems (steps 5 and 8 in Algorithm 1). Additionally, the multiplication with the error-free matrix C needed at each refinement iteration when the residual is computed needs to be done based on Equation (3.6) and requires additional second-stage linear solves. We have tested this technique and observed longer execution times due to considerable load imbalance caused by different number of iterative refinement iterations in the independent second-stage solves.

Algorithm 2 lists the preconditioned BiCGStab algorithm. All vector operations (dotproducts, addition, and scalar multiplications) are performed in parallel (no communication occurs since the first-stage part of the vectors is replicated across all processes). The multiplication v = Kz is also performed in parallel and requires only reducing vector  $v_0 =$ 

### **Algorithm 3**

Solving  $K\Delta z = r$  in parallel with BiCGStab using  $\tilde{K}$  as preconditioner

```
Given the set \mathscr{P}=\{1,2,\ldots,P\} processes, distribute N scenarios evenly across \mathscr{P} and let \mathscr{N}_p be the set of scenarios assigned to process p\in\mathscr{P}. Each process p\in\mathscr{P} executes: (preconditioner\ computation) \qquad (\tilde{L}_1,\tilde{D}_1,\ldots,\tilde{L}_N,\tilde{D}_N,\tilde{L}_C,\tilde{D}_C) = \operatorname{Factorize}(\tilde{K}) (BiCGStab\ solve\ phase) \qquad \Delta z = \operatorname{BiCGStabSolve}(K,\tilde{L}_1,\tilde{D}_1,\ldots,\tilde{L}_N,\tilde{D}_N,\tilde{L}_C,\tilde{D}_C,r).
```

 $\sum_{i=1}^{N} B_i^T z_i + K_0 z_0$ . The application of the preconditioner, that is, computing  $y = \tilde{K}^{-1} v$ , is the most computationally expensive part and is done by calling the *Solve* function listed in Algorithm 1. Factors  $\tilde{L}_1, \tilde{D}_1, \ldots, \tilde{L}_N, \tilde{D}_N, \tilde{L}_C, \tilde{D}_C$  of  $\tilde{K}$  are obtained by calling the *Factorize* function of Algorithm 1 before the BiCGStab iteration starts. The complete computational procedure of solving the linear system with BiCGStab is summarized in Algorithm 3.

- **4. Numerical Experiments.** In this section we report the parallel performance and efficiency of PIPS-IPM equipped with the PARDISO implementation of the augmented approach.
- **4.1. Experimental testbed.** Before moving on to the parallel scalability benchmarks of the stochastic optimization application, we briefly describe the target hardware, namely, a IBM BG/P at the Argonne Leadership Computing Facility (ALCF) and a Cray XE6 system installed at the Swiss National Supercomputing Center CSCS.

Large-scale runs were performed on the "Intrepid" BG/P supercomputer that has 40 racks with a total 40,960 nodes and a high-performance interconnect. Small-scale experiments were performed on "Challanger" BG/P which consists of only one rack (1024 nodes) and is intended for small test runs. Each BG/P node has a 850 MHz quad-core PowerPC processor and 2 GB of RAM.

The Cray XE6 has 1,496 compute nodes, each of the compute nodes consisting of two 16-core AMD Opteron 6,272 2.1 GHz Interlagos processors, giving 32 cores in total per node with 32 GBytes of memory. In total there are 47,872 compute cores and over 46 Terabytes of memory available on the compute nodes. The Interlagos CPUs implement AMD's recent "Bulldozer" microarchitecture; each Interlagos socket contains two dies, each of which contains four so-called modules.

- **4.2. Intranode performance.** The first set of experiments investigates the speed-up of the augmented approach over the previous "backsolve" approach when computing the scenario contribution  $B_i^T K_i^{-1} B$  to the Schur complement. The augmented Schur complement approach described in the previous section was implemented in the PARDISO solver package<sup>1</sup>; it will be refereed as PARDISO-SC, whereas we will use the acronym PARDISO for the traditional "backsolve" approach.
- **4.2.1. Artificial test scenarios.** In this section we consider a PDE-constrained quadratic program that has been used in [21] to compare the backsolve approach with the augmented approach. The matrix  $K_i$  in all test scenarios are matrices obtained after a seven-point finite-difference discretization of a 3D Laplacian operator.

<sup>&</sup>lt;sup>1</sup>http://www.pardiso-project.org

TABLE 4.1

Average times in seconds (and Gflop/s in brackets) for different number of Cray XE6 and BG/P cores inside PARDISO and PARDISO-SC when computing the Schur complement contribution  $B_i^T K_i^{-1} B$  with n = 97,336 columns in  $K_i$  and 2,921 sparse columns in  $B_i$ . The artificial test scenarios in  $K_i$  are similar to the discretization of a Laplacian operator on a 3D cube.

Number	AMD	Interlagos	BG/P		
of cores	PARDISO	PARDISO-SC	PARDISO	PARDISO-SC	
1	454.64	14.43 ( 5.28)	3,011.78	101.09 (0.72)	
2	301.03	11.53 (7.19)	1,506.97	51.23 (1.42)	
4	202.54	6.35 (13.62)	905.83	26.30 (2.72)	
8	153.14	4.35 (20.38)	_	_	
16	92.06	2.84 (31.68)	_	_	
32	83.96	1.79 (41.01)	_	_	

Table 4.2

Average times in seconds inside PARDISO and PARDISO-SC when computing the Schur complement contribution  $B_i^T K_i^{-1} B$  with  $n_c$  columns in  $K_i$  and  $0.03 \cdot n_c$  sparse columns in  $B_i$ . The artificial test scenarios in  $K_i$  are similar to the discretization of a Laplacian operator on a 3D cube.

$n_c$	AMD	Interlagos	]	3G/P
	PARDISO	PARDISO-SC	PARDISO	PARDISO-SC
9,261	0.61	0.02	2.95	0.13
27,000	8.32	0.28	35.85	1.48
97,336	202.54	6.25	905.83	26.30
341,061	11,009.46	508.14	19,615.17	2,431.31

Table 4.1 compares runtime in seconds for different numbers of Cray XE6 and BG/P cores inside PARDISO and PARDISO-SC when computing the Schur complement contribution  $B_i^T K_i^{-1}B$ . The matrix  $K_i$  used in this experiment has n = 97,336 columns and rows and is augmented with a matrix  $B_i$  with 2,921 columns. The nonzeros in the matrix  $B_i$  are randomly generated, with one nonzero element in each column. Table 4.1 shows strong scaling results for 1 to 32 threads. In fact, the table shows that the exploitation of sparsity in the augmented approach is highly beneficial and can accelerate the overall construction of the Schur complement matrix C up to a factor of 32 on one core. It is also demonstrated that the augmented approach results in better scalability on multicores on both the AMD Interlagos and the BG/P cores.

Table 4.2 demonstrates the impact of the augmented approach for various matrices  $K_i$ . The size of the matrices increases from  $n_c = 9,261$  columns up to  $n_c = 341,061$  columns. The matrix  $B_i$  always has  $0.03 \cdot n_c$  nonzero columns. We always used four Interlagos and BG/P cores for these experiments. The timing numbers in the table show that the execution speed always increases linearly by a factor close to 32 independent of the number of columns and rows in  $K_i$ .

Table 4.3 compares the influence of the number of columns in  $B_i$  for both the backsolve and the augmented approach. In this case, the number of columns and rows in the matrix  $K_i$  is always constant with n = 97,336 and, again, we always used four Interlagos and BG/P cores. We varied the number of columns in  $B_i$  from 2,920 columns (k = 0.03) to 19,467 columns (k = 0.20). Although both approaches are mathematically equivalent, the efficiency of the PARDISO-SC implementation is considerable and compelling for problems with large

TABLE 4.3

Average times in seconds for  $k \cdot n$  numbers of sparse columns in  $B_i$  inside PARDISO and PARDISO-SC when computing the Schur complement contribution  $B_i^T K_i^{-1} B$ . The artificial test problem in  $K_i$  is similar to the discretization of a Laplacian operator on a 3D cube and always has n = 97,336 columns

k	AMD	Interlagos	I	3G/P
	PARDISO	PARDISO-SC	PARDISO	PARDISO-SC
0.03	202.54	6.25	905.83	26.30
0.06	392.32	21.85	1,869.01	100.96
0.10	667.42	70.22	2,987.81	280.31
0.13	952.40	159.90	3,351.10	556.98
0.16	1,108.20	233.65	4,886.56	1,046.43
0.20	1,334.68	359.32	5,977.95	1,608.01

TABLE 4.4

Computation times of the Schur complement contribution  $B_i^T K_i^{-1} B_i$  on BG/P nodes for stochastic problems with 6-, 12- and 24-hour horizon. The dimensions of  $K_i$  and  $B_i$  are shown in Table 4.5.

Test	$B_i^T K_i^{-1} B$ time (sec)			
Problem	Cores	PARDISO	PARDISO-SC	
UC6	1	109.21	9.31	
	2	58.85	5.58	
	4	32.01	4.09	
UC12	1	481.89	58.41	
	2	250.77	34.36	
	4	136.97	22.50	
UC24	1	1,986.35	273.28	
	2	1,090.01	167.47	
	4	568.78	94.72	

scenarios and smaller Schur complement matrices with up to 20% of columns in  $B_i$ .

**4.2.2.** Stochastic optimization problems. We solved 4-hour, 12-hour, and 24-hour horizon instances (denoted by UC4, UC12 and UC24) of the stochastic unit commitment with 32 scenarios on 32 nodes of "Challenger" (1 MPI process per node). For PARDISOSC we ran with 1, 2, and 4 threads and report the intranode scaling efficiency. When using PARDISO, PIPS uses the multiple right-hand side feature of the solver in computing  $K_i^{-1}B$ . The execution times represent the average (computed over the scenarios and first 10 IPM iterations) time needed for one scenario. We do not list the standard deviation since it is very small (less than 1%). Table 4.4 summarizes the results.

The shared-memory parallel efficiency of the PARDISO-SC implementation is considerable and compelling for problems with large scenarios (such as UC12 and UC24) that requires one dedicated node per scenario. In addition, the speed-ups over the classical "backsolve" approach show great potential in achieving our goal of considerably reducing the time to solution for problems with a large number of scenarios.

**4.3.** Large-scale runs. Following the great intranode speed-up showed by PARDISO-SC approach, in this section we investigate and report on the large-scale performance of PIPS equipped with PARDISO-SC in solving the UC instances. We look at several performance

TABLE 4.5 Sizes of  $K_i$  and  $B_i$  for UC instances with 6-, 12-, and 24-hour horizon.

UC Instance	Size K <sub>i</sub>	# Nonzero Cols. of $B_i$
UC6	59,478	1,566
UC12	118,956	3,132
UC24	237,912	6,264

metrics, that are relevant both to the application (such as time-to-solution) and to the parallel efficiency of the implementation (such as scaling efficiencies and sustained FLOPS performance of PIPS).

The BG/P "Intrepid" experiments solve the UC12 instances with up to 32,768 scenarios on up to 32,768 nodes (131,072 cores). UC24 problems are too big to fit even 1 scenario per node for large number of scenarios/nodes. All runs of this section were run in "SMP" mode, which means one MPI process per node using four cores. All problems solved terminated with  $\mu < 10^{-8}$  and  $\|r\| < 10^{-8}$ , standard termination criteria for interior-point methods [28].

**4.3.1. Time to solution.** As we mentioned in Section 2, industry practice is to solve UC procedures under strict "real-time" requirements, which in the case of the UC models mean solving the problems under 1 hour (size of the time horizon step). To test the real-time capabilities of PIPS with PARDISO-SC, we solved UC12 instances with an increasingly larger number of scenarios and nodes (one scenario per node). The total execution times shown in Table 4.6 are within the requirements. Moreover, the speed-up over the previous "backsolve" approach is substantial. With the "backsolve" approach and using the MA57 as linear solver, PIPS needed *4 hours 3 minutes* to solve a *UC4* instance with 8,192 scenarios and 8,192 nodes on the same architecture. With the augmented factorization implementation from PARDISO-SC, a *UC12* instance with the same number of scenarios can be solved in a little more than 1 hour using the same number of nodes.

TABLE 4.6

Solve times in seconds, number of iterations, and average time per iteration needed by PIPS to solve UC12 instances with increasingly large number of scenarios. One scenario per node was used in all runs.

No. of Nodes and Scenarios	Solve Time in minutes	IPM Iterations	Average Time per Iteration in seconds	Size of <i>K</i> in billions
4,096	59.14	103	33.57	0.487
8,192	64.72	112	34.67	0.974
16,384	70.14	123	34.80	1.949
32,768	79.69	133	35.95	3.898

Additional reduction in the total execution time can be obtained by reusing solution information from the UC instance solved previously, a process known as warm-starting. Reduction of 30-40% in the iteration count has been reported for interior-point methods applied to stochastic programming [8]. On the other hand, finding a binary integer solution requires additional effort, as described in Section 2. While this is future research, we expect the cost of this phase to be low and not to offset the gains obtained by warm-starting.

The solve time increases with the number of scenarios because the optimization problems are larger and require a larger iteration count. However, even though the size of the problem increases by 8 times from 4K to 32K, the iteration count shown in Table 4.6 increases by less

than 30%. This is due to the excellent iteration complexity  $(O(\sqrt{n}\log(1/\varepsilon)))$ , where n is the dimension of the problem and  $\varepsilon$  is the accuracy (in our case  $10^{-8}$ ) of the primal-dual path-following interior-point methods such as Mehrotra's predictor-corrector we implemented.

The average cost of an interior-point iteration increases by only 7% from 4K scenarios to 32K scenarios, mainly because of a small increase in the number of BiCGStab iterations (more pivot perturbation errors are present and need to be absorbed as the scenario count increases), and not to communication or load imbalance overhead.

**4.3.2. Breakdown of the execution time.** In Figure 4.1 we show the execution time and the three most expensive components of the execution time for each interior-point iteration for our largest simulation (UC12 with 32,768 scenarios on 32,768 nodes).

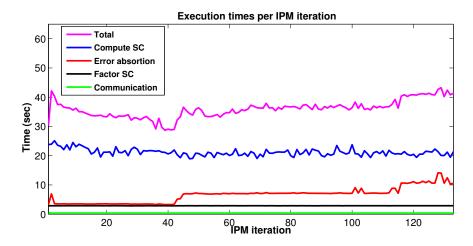


FIG. 4.1. Breakdown of the execution time per interior-point iteration for UC12 with 32K scenarios on 32K nodes. The total execution time and its three most expensive components are shown. "Compute SC" represents average times of computing  $B_i^T K_i^{-1} B_i$ , "Error absorbtion" shows the cost of BiCGStab procedure and "Communication" indicates the communication time.

The "Compute SC" displays the time needed by PARDISO-SC to compute the Schur complement contribution  $B_i^T K_i^{-1} B_i$ , averaged across all scenarios. "Error absorbtion" indicates the time spent in the BiCGStab procedure that absorbs the errors in K that occurred because of the pivot perturbations in PARDISO-SC. "Factor SC" represents the time spent in the dense factorization of  $\tilde{C}$ . "Communication" depicts the total time spent in internode communication in both the "Compute-SC" and "Error absorbtion" computational steps.

As can be seen in Figure 4.1, the overhead of the error absorbtion steps is fairly low (ranges from 10% to 40% of the "Compute SC" step) and plays a crucial role in the speed-ups in the time to solution of the PARDISO-SC approach over the previous "backsolve" approach from PIPS [14].

The cost increase of the error absorbtion phase with the interior-point iterations is due to an increase in the number of BiCGStab iterations. This behavior is most likely caused by an increasingly ill-conditioning of the linear systems  $K_i$  (a well-known behavior of interior-point methods) that amplifies the pivot perturbation errors and, consequently, decreases the quality of the preconditioner  $\tilde{C}$ . The average number of BiCGStab iterations ranges from 0 in the incipient phases of the optimization to 1.5 near the solution.

The very low cost of the communication (< 0.4 seconds) is the effect of the very fast BG/P communication collectives routines. Other parallel computing systems may show different behaviours, depending on the speed on the network. However, the next-generation

supercomputers (such as BG/Q) will operate even faster networks, and so it is very unlikely that the communication will become a bottleneck on this architecture.

**4.3.3. Parallel efficiency.** Scalability studies of this section are aimed at providing a lightweight measurement of the efficiency of PIPS-IPM and an extrapolation basis for the performance of PIPS-IPM for larger problems.

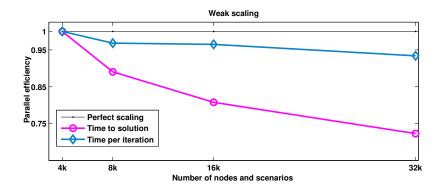


FIG. 4.2. "Weak scaling" efficiency plot showing the parallel efficiency of PIPS in solving UC12 instances on "Intrepid." The closer to "Perfect scaling" horizonal line, the better, meaning that there is less parallel efficiency loss.

We first present the so-called weak-scaling efficiency study. It consists of solving increasingly large problems with additional computational resources. In our study we used UC12 instances, and we increased the number of scenarios and nodes at the same rate (their ratio is 1). Linear scaling or perfect efficiency is achieved if the run time stays constant. The efficiency of PIPS-IPM is shown in Figure 4.2. We have used the same UC12 instances and run times of Table 4.6. The efficiency of the entire optimization process (excluding problem loading) is about 74%, and the efficiency of the parallel linear algebra is more than 94% percent. In our opinion these are very good parallel efficiencies, in part due to the fast BG/Q collectives (relative to the speed of the cores) and in part to a judicious use of MPI and numerical linear algebra libraries in our implementation. The reduced efficiency of the overall optimization process is caused mostly by the increase in the number of optimization iterations, as we pointed out in Section 4.3.1.

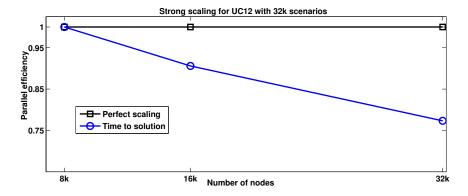


Fig. 4.3. "Strong scaling" efficiency plot showing the parallel efficiency of PIPS in solving UC12 instance with 32k scenarios on 8k, 16k and 32k nodes of "Intrepid" BG/P.

The second efficiency study fixes the size of the problem and increases the number of computational nodes. This is known as a strong-scaling study. An application scales linearly if the run time decreases at the same rate the number of nodes increases. In our experiments we solved the UC12 instance with 32K scenarios on 8K, 16K and 32K nodes (the run on 4K nodes ran out of memory). The strong scaling efficiency displayed in Figure 4.3 is over 75% from 8K to 32K nodes. The slight deterioration in the efficiency is caused mainly by the dense factorization of the Schur complement and solves with its factors, which is replicated across all computational nodes. Reducing the cost of this dense linear algebra calculations (for example, by using GPU acceleration) would greatly improve the strong-scaling efficiency of our code.

We also report the performance of PIPS-IPM in terms of sustained floating-point operations per second (Flops). This is a measure of how efficiently are involved the cores in computations. For this experiment we solved UC12 with 4K scenarios on 4K nodes (1 BG/P rack) and used the Hardware Performance Monitor (HPM) library to count the number of floating-point operations. The sustained Flops rate of PIPS-IPM on 1 rack was 3.37 TFlops, which accounts for 6.05% of theoretical FLOPS peak rate. Inside our code, the two largest Flops rates were attained by the PARDISO augmented factorization, 4.21 TFlops (7.55% of peak) and LAPACK symmetric indefinite factorization routine DSYTRF 14.75 TFlops (26.48% of peak). With LAPACK we used ESSL Blue Gene SMP BLAS provided by the computer manufacterer (IBM). We caution that sustained Flops performance may not be the best measure of efficiency in our case because a large proportion of computations is dedicated to sparse linear algebra, which by definition is difficult to vectorize and requires additional integer arithmetic (fixed-point operations) that are not counted by HPM. However, this work improved the Flops rate of PIPS-IPM of by a factor of 6 over the previously used "backsolve" technique, because of the efficient core utilization in the computation of the Schur complement terms  $B_i^T K_i^{-1} B_i$ .

**5. Conclusions.** This paper presents a novel technique for the computation of Schur complement matrices that occur in decomposition schemes for the solution of optimization problems with dual-block angular structure. This class of structured optimization problems includes stochastic optimizations problems with recourse, an important class of problems for addressing the difficult issue of integrating renewable sources of energy into the power grid. We present and discuss a stochastic model for the real-time unit commitment problem.

Based on an incomplete sparse factorization of an augmented system that is implemented in PARDISO, the proposed approach is capable of using multicore nodes efficiently, as well as fully exploiting sparsity. The pivot perturbation errors are not managed for each scenario independently; instead, we use preconditioned BiCGStab to absorb the errors at once, for all scenarios. This approach maintains good load balancing even when tens of thousands of nodes are simultaneously involved in computations. The implementation of the augmented approach in the PIPS optimization solver showed substantial speed-up over the previous approach. PIPS solved realistically sized instances (12-hour horizon and state of Illinois power grid) of the stochastic unit commitment with thousands of scenarios in about 1 hour on the "Intrepid" BG/P supercomputer.

Our future work will attempt to solve larger horizon problems on Argonne's "Mira" BG/Q supercomputer. BG/Q platforms have four times more cores per node and 8 times more and substantially faster memory as BG/P platforms. Additionally, BG/Q cores are running twice the clockrate of BG/P cores, and the BG/Q interconnect is about 4 times faster. We strongly believe that a 24-hour stochastic unit commitment with thousands of scenarios can be solved on "Mira" under the strict real-time requirements of the power grid industry.

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